Senior Firmware-Gateware Engineer

Please submit resume to [jeff.sondermeyer@coachcomm.com](mailto:jeff.sondermeyer@coachcomm.com) for consideration.

## Position Summary

The Senior Firmware-Gateware Engineer position will be located in our Huntsville, AL engineering office. The candidate must possess strong organizational and problem solving skills. This position will design and develop embedded C/C++ firmware targeting microcontrollers and DSPs (used as a microcontroller). The candidate must some experience and background in FPGA HDL development specifically with VHDL. This employee will work closely with our development team and the Director of Engineering.

## Responsibilities:

* Cooperate as part of an inter-discipline team and coordinate with other developers and departments.
* Manage assignments and complete tasks to achieve targeted objectives.
* Communicate timely and concisely with development team and management to identify and solve issues.

## Minimum Qualifications

* 10+ years of applicable industry experience designing, developing, embedded C/C++ firmware and VHDL gateware.
* An eagerness to learn and a teachable attitude.
* Proven organizational and time management skills.
* Excellent problem solving and analytical skills.
* Good English communication skills.
* Robust experience using RTOS and TCP/IP stacks.
* Industry experience targeting microcontrollers and DSPs (using DSP as microcontroller).
* Experience debugging using JTAG.
* Experience with scripting languages to manage software tools and build processes.
* Experience using version control systems to manage source code revisions.
* Experience with sound documentation and code commenting practices.

## Preferred Qualifications

* At least 3 years of applicable experience with FPGA fabric design, debug and implementation (preferred Xilinx)
* Experience architecting and designing embedded systems.
* Experience with MicroBlaze, FreeRTOS, and LwIP.
* Experience with external interfaces and peripherals: I2C, SPI, I2S, Flash, SDRAM, ADC/DAC, MAC/PHY.
* Experience with audio processing, CODECs, and DSP libraries.
* Experience using lab instruments such as oscilloscopes, logic analyzers, power supplies, etc.
* Experience using the Xilinx Vivado/SDK/Vitis Design Suite.
* Experience using the TI Code Composer Suite.
* Experience developing user interfacing applications.
* Experience with Python and Tcl.
* Experience with Agile methodologies and Jira.
* Experience using Git and SVN version control systems.

## Educational Requirements

* BSEE, BSCE, BSCS or equivalent from an accredited university. MSEE, MSCE, MSCS or equivalent preferred. Additional experience will be considered in lieu of a degree.

## Job Type:

* Fulltime

## Required work authorization:

* United States citizen